

A digital-to-analogue converter is hardly a cheap alternative to a potentiometer. However, some readily pay the price, only to get rid of the "dirty" mechanical contact, while others are prepared to pay for remote control and repeatable settings. Erik Margan describes an ingenious design using a single D-to-A back to front.

Single D-to-A equalisation

In contrast with other digitally controlled equalisation circuits, which use either a D-to-A to set the analogue voltage for driving two VCAs, or use two D-to-As to provide the required boost-cut function, the circuit described below exploits the available complementary outputs of some cmos D-to-As, like AD7524, to achieve the same result with only one D-to-A.

Actually, the D-to-A is connected backwards; the complementary outputs function as inputs and the voltage reference input is used as output. This is possible due to bidirectional cmos switches in the D-to-A and the symmetrical $\pm 7.5\text{Vdc}$ power supply for the D-to-A and control logic. Since the voltage swing capability of D-to-A outputs is limited, two resistive dividers level the signal down and an op-amp is used to restore gain and compensate for filter losses.

This op-amp also serves as a low-impedance driver for the filter section, thus preventing filter transfer function variations with boost/cut setting (a weak point of many equalisers using gyrators).

The second op-amp boosts or cuts the filter output by

$$G = 2 + \frac{R_m}{R_n} = 4$$

multiplied by the 4-bit digital code in symmetrical offset-binary format (0111 = 1000 = 0dB).

For the band-pass filter I have used a series-shunt RC network. Its transfer function is:

$$F(j\omega) = \frac{R_b}{R_a + \frac{j\omega C_a R_b + 1}{j\omega C_b R_b + 1}}$$

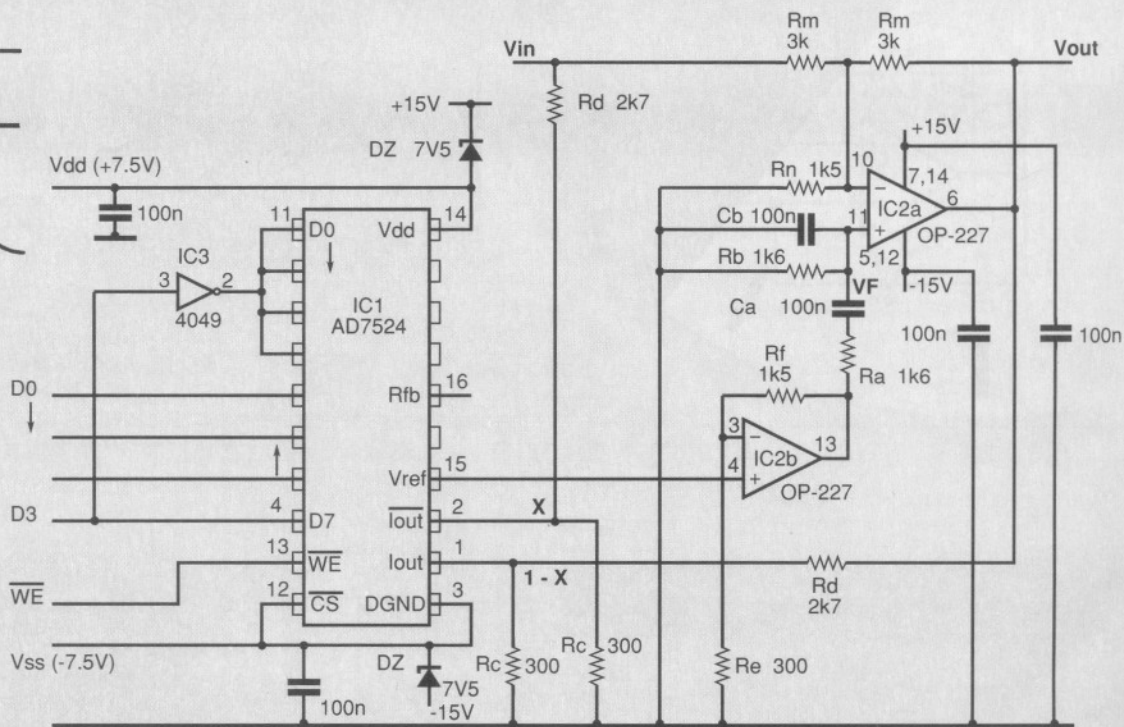
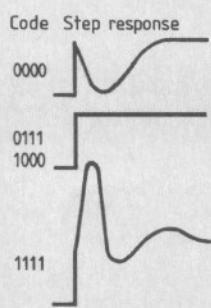


Fig.1. Circuit diagram. Note the $\pm 7.5\text{V DC}$ power supply for the D-to-A and the data control logic.

With little rearrangement, it may be rewritten as:

$$G(j\omega) = \frac{1}{R_a C_b} j\omega \frac{1}{(j\omega)^2 + j\omega \left(\frac{1}{R_a C_a} + \frac{1}{R_b C_b} + \frac{1}{R_a C_b} \right) + \frac{1}{R_a C_a R_b C_b}}$$

If we compare this to the normalized bi-quadratic form:

$$H(s) = A_0 \frac{\omega_0 s}{s^2 + \frac{\omega_0}{Q} s + \omega_0^2}$$

we find the filter centre frequency at:

$$\omega_0 = \sqrt{\frac{1}{R_a C_a R_b C_b}}$$

From the last three equations we may calculate the filter Q-factor:

$$Q = \frac{1}{\frac{1}{R_a C_a} + \frac{1}{R_b C_b} + \frac{1}{R_a C_b}} = \frac{1}{\frac{1}{R_a C_a} + \frac{1}{R_b C_b} + \frac{1}{R_a C_b}}$$

and the attenuation at the centre frequency:

$$\frac{1}{R_a C_b} \cdot \frac{1}{\frac{1}{R_a C_a} + \frac{1}{R_b C_b} + \frac{1}{R_a C_b}} = \frac{1}{C_b + \frac{R_a}{R_b} + 1}$$

If $R_a = R_b = R$ and $C_a = C_b = C$ then

$$\omega_0 = \frac{1}{RC} \quad Q = \frac{1}{3} \quad A_0 = \frac{1}{3}$$

and this results in one-octave band-width. The filter output voltage is:

$$V_F = (xV_{in} + (1-x)V_{out}) a_1 A_1 F(j\omega)$$

where:

$$0 \leq x \leq 1 \quad a_1 = \frac{R_d}{R_c + R_d} = \frac{1}{10} \quad A_1 = 1 + \frac{R_e}{R_f} = 6$$

The system output voltage is then:

$$V_{out} = -V_{in} + a_1 A_1 G V_F$$

So the system transfer function will be:

$$\frac{V_{out}}{V_{in}} = \frac{1 - x a_1 A_1 G F(j\omega)}{1 - (1-x) a_1 A_1 G F(j\omega)}$$

Of course, x is digital code dependent:

$$x = \frac{D_7 2^7 + D_6 2^6 + D_5 2^5 + D_4 2^4 + \overline{D_3} (2^3 + 2^2 + 2^1 + 2^0)}{2^8 - 1}$$

Figure 1 shows the circuit schematic diagram and Fig 2 shows the frequency responses for various settings. To make the settings symmetrical, I used a 4-bit code with the MSB inverted and tied to the lower four bits. Due to the $x/(1-x)$ law, the increment at the filter centre frequency is 1.5dB around code 1000, rising up to 2.2dB at extremes. Of course, up to 8-bit control is possible.

Other equalizing sections are connected seri-

ally. It is possible to use two separate virtual-earth summing junctions for a parallel connection of sections and the D-to-A is then connected in the usual way, but the result is much more noisy. The serial system single-stage noise gain is low ($G = 4$, with no boost or cut), the noise of other stages is passed unaffected and the D-to-A noise (the dominant noise source) is filtered in one-octave bands, so a 10-band system will be as noisy as a single

wideband stage. Use low impedances wherever possible and low-noise op-amps like the dual OP227.

The maximum input signal level is $2V_{pp}$, which should allow for boost on all stages without severe distortion and a good signal-to-noise ratio. With filters one-octave wide, some mutual influence of adjacent bands is inevitable (see Fig 3). If this is undesirable, use more elaborate filters

