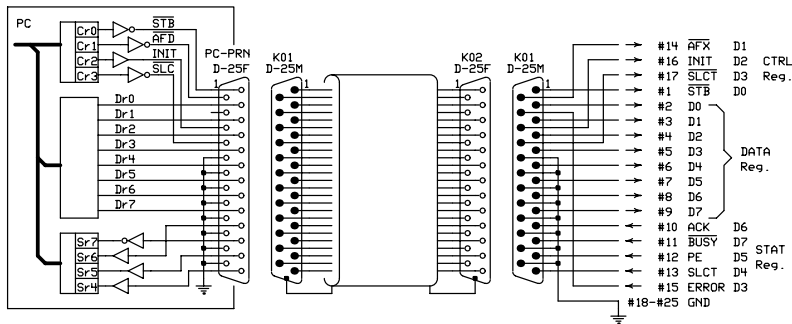


Parallel Port Address Map			
Port	Data-Reg (base)	Status-Reg.	Control-Reg.
LPT1	0x378	0x379	0x37A
LPT2	0x278	0x279	0x27A
LPT3	0x3BC	0x3BD	0x3BE

NOTE : Some PCs have LPT1 ↔ LPT3

Status-Reg.			
Bit	Function	pin	inverted
0,1,2	not used	-	-
3	ERROR	15	no
4	SLCT (on-line)	13	no
5	PE (paper out)	12	no
6	ACK (IRQ)	10	no
7	BUSY	11	yes

Control-Reg			
Bit	Function	pin	inverted
0	STROBE	1	yes
1	AUTO-PD-XT	14	yes
2	INIT	16	no
3	SLCT-in	17	yes
4	IRQ-Enable (IRQ7)	-	-
5,6,7	not used	-	-



Institut Jožef Stefan, Ljubljana

projekt/naloga : **Photo-Diode Amplifier + ADC**

sestavni del : **Shema+Protocol**

priporočila : tolerance uporov 1 %
tolerance kondenzatorjev 20 %

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list : 1/1
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