



	A15	A14	A13	ADDRESS DECODER			
ADDR : \$8000 - \$FFFF	1	X	X				
ADDR : \$6000 - \$7FFF	0	1	1				
ADDR : \$4000 - \$5FFF	0	1	0				
ADDR : \$2000 - \$3FFF	0	0	1				
ADDR : \$0000 - \$1FFF	0	0	0				
				A7	A6	A5	A4
				0	0	1	X
				0	1	0	X

Address Space

HD63B03

TEST Trigger ON (0)	# 22A	P10
--- Not used! ---	# 22B	P11
HD Ready	# 21A	P12
9th ADDR, HD	# 21B	P13
Keyboard bit 0	# 20A	P14
Keyboard bit 1	# 20B	P15
Keyboard bit 2	# 19A	P16
Keyboard bit 3 + NMI	# 19B	P17

Int EPROM Enable (1)	# 25A	P20
TEST EOS (TIMER)	# 25B	P21
Int NMI->IRQ	# 24A	P22
TEST CONU. (MM)	# 24B	P23
Int Disable NMI	# 23A	P24

\$0000	32 Bytes	Internal Registers
\$001F		
\$0020	32 Bytes	External Pn
\$003F		
\$0040	16 Bytes	External CSn
\$004F		
\$0050	48 Bytes	Not used
\$007F		
\$0080	128 Bytes	Internal RAM
\$00FF		
\$0100	16k-256 Bytes	Not used
\$3FFF		
\$4000	16k Bytes	External RAM
\$7FFF		
\$8000	32k Bytes	External ROM
\$FFFF		

Institut Jožef Stefan, Ljubljana

projekt/naloga :	Iskrascope LCD - MPU		
sestavni del :	Timing & AddrSp	načrtoval	PS,SU,TP,EM
pripombe :	tolerance uporov 5 %	risal	E. Margan
	tolerance kondenzatorjev 20 %	datum	1985.05.03.
		list	1/1
		koda	F537-U2-X1