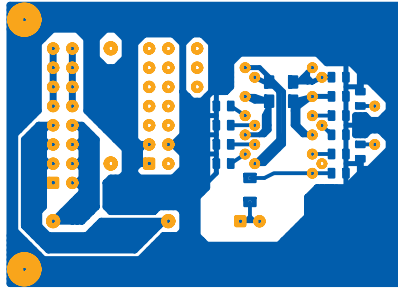
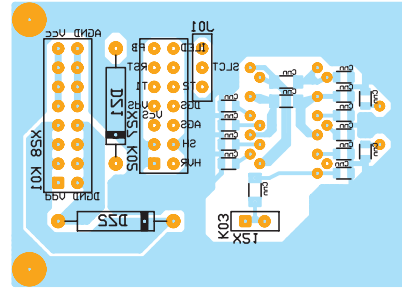


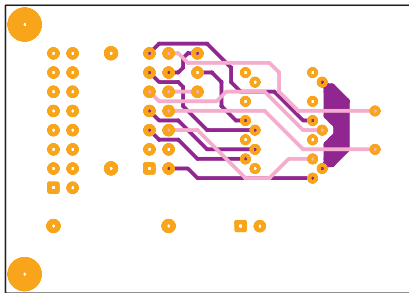
TPPF1-D PCB, ver.1
(1 cell, actual config. 3x3)
2003.11.11.



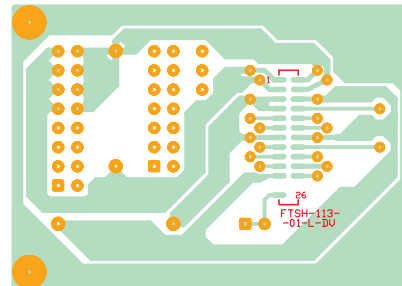
Bottom Layer (1)



Bottom Silk Screen (254)



Layer 3
Layer 4



Top Layer (2)
Top Silk Screen (255)