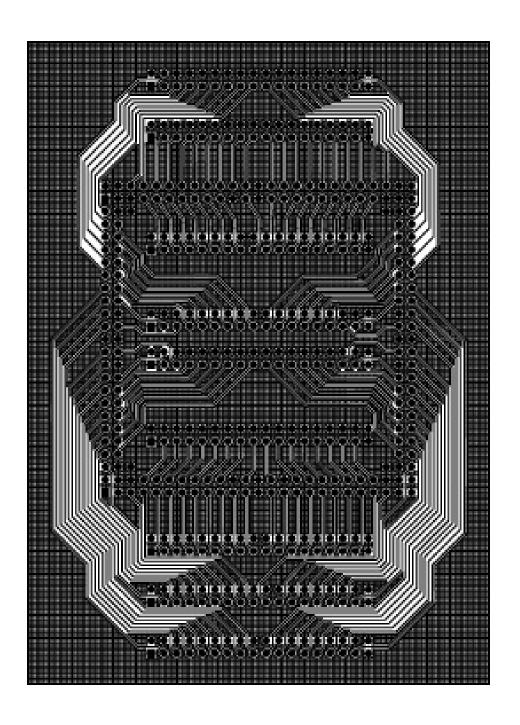# Preparation of a back-up for the HAPD read-out

Andrej Gorisek

Idea: have a solution ready for the beam test which would be based on the ASD8 read-out chips (same as were used for the Burle MCP PMT)

Status: interface board (plug-in for HAPD, signal routing, plug-in for ASD8 boards): design will be ready later today, board produced on Tuesday, check the connections

INTERFACE BOARD: layout of one of the layers with HAPD pins, ASD8 pins and the signal routing lines

Interface board, continued: checks of interconnections later next week. Samo and Peter will bring the board to KEK on June 17.

Test of ASD8 read-out with an HAPD

-tests with the TO HAPD will be done today

-on Tuesday we will get the 3x3 HAPD and test it