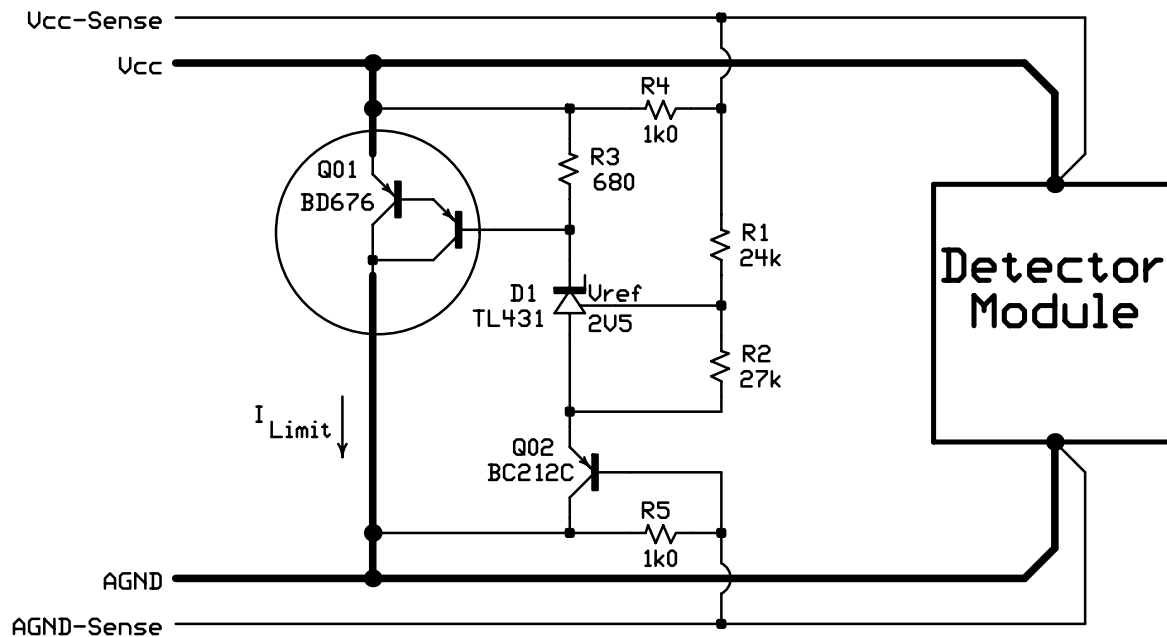


Patch-Panel-2 Power-Supply Limiter New Proposal, March 2001

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R4 and R5 are safety resistors.

$$U_{Limit} = U_{ref} * (R1 + R2) / R2 + U_{be}(Q2)$$

$$I_{Limit} = (U_{cc\ Max} - U_{Limit}) / 2 * R_{cable}$$

The voltage limit on the D.M. is independent of the cable voltage drop.

 Institut Jožef Stefan

projekt/naloga :	Atlas Barrel, Patch-Panel-2			
sestavni del :	Power Limiter	načrtoval	E. Margan	
pripombe :	tolerance uporov	1 %	risal	E. Margan
			datum	2001.03.03.
			list	1/1
			koda	F9AB-U4-XFP