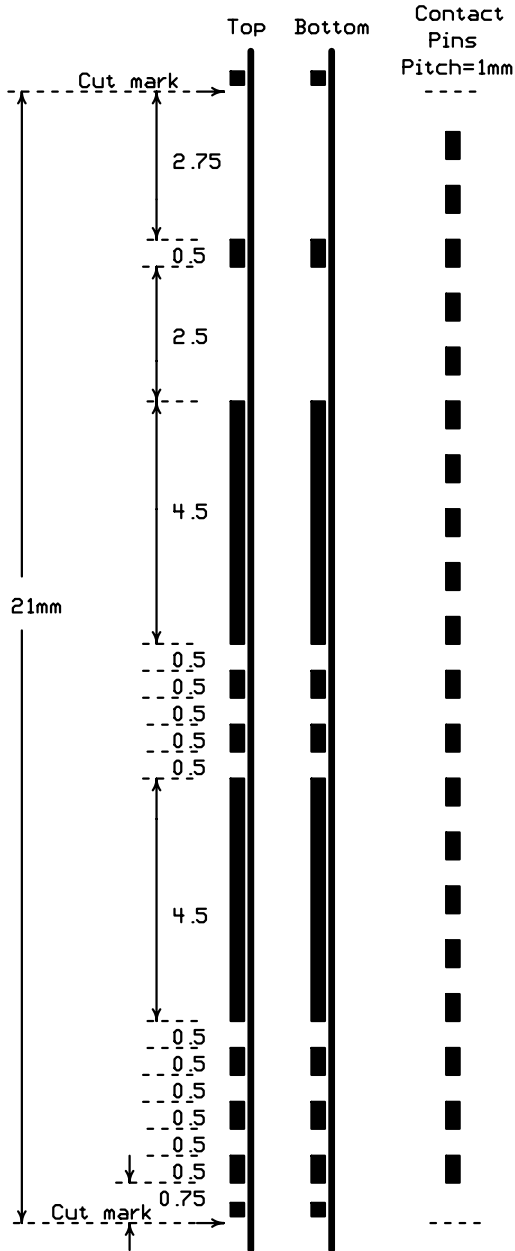


# Cable Layout Proposal - A

Date : Dec. 07 1999

## Cable Layout



## Pin Assignment

	JA	JB
1	not used	1 not used
2	not used	2 not used
3	BIAS (AG)	3 BIAS HV
4	not used	4 not used
5	not used	5 not used
6	AG	6 Vcc
7	AG	7 Vcc
8	AG	8 Vcc
9	AG	9 Vcc
10	AG	10 Vcc
11	AG Sense	11 Vcc Sense
12	DG Sense	12 Vdd Sense
13	DG	13 Vdd
14	DG	14 Vdd
15	DG	15 Vdd
16	DG	16 Vdd
17	DG	17 Vdd
18	Temp1	18 PIN Bias
19	Temp2 (RESET)	19 Select
20	ILEDa	20 ILEDb

## Cable Layout Sample

